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P. 3/18

DEC 09 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:)	
BEAUJOIN ET AL.)	
)	
Serial No. 10/075,113)	Examiner: J. TABONE, JR
)	
Confirmation No: 6957)	Art Unit: 2133
)	
Filing Date: FEBRUARY 13, 2002)	
)	
For: METHOD OF TESTING A SEQUENTIAL)	
ACCESS MEMORY PLANE AND A)	
CORRESPONDING SEQUENTIAL ACCESS)	
MEMORY SEMICONDUCTOR DEVICE)	
)	

REPLY

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Responsive to the Office Action of September 9,
2004, please consider the remarks set out below.

Amendments to the Claims are reflected in the listing of
claims which begins on page 2 of this filing.

Remarks begin on page 11 of this filing.